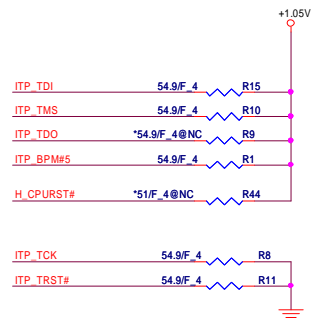
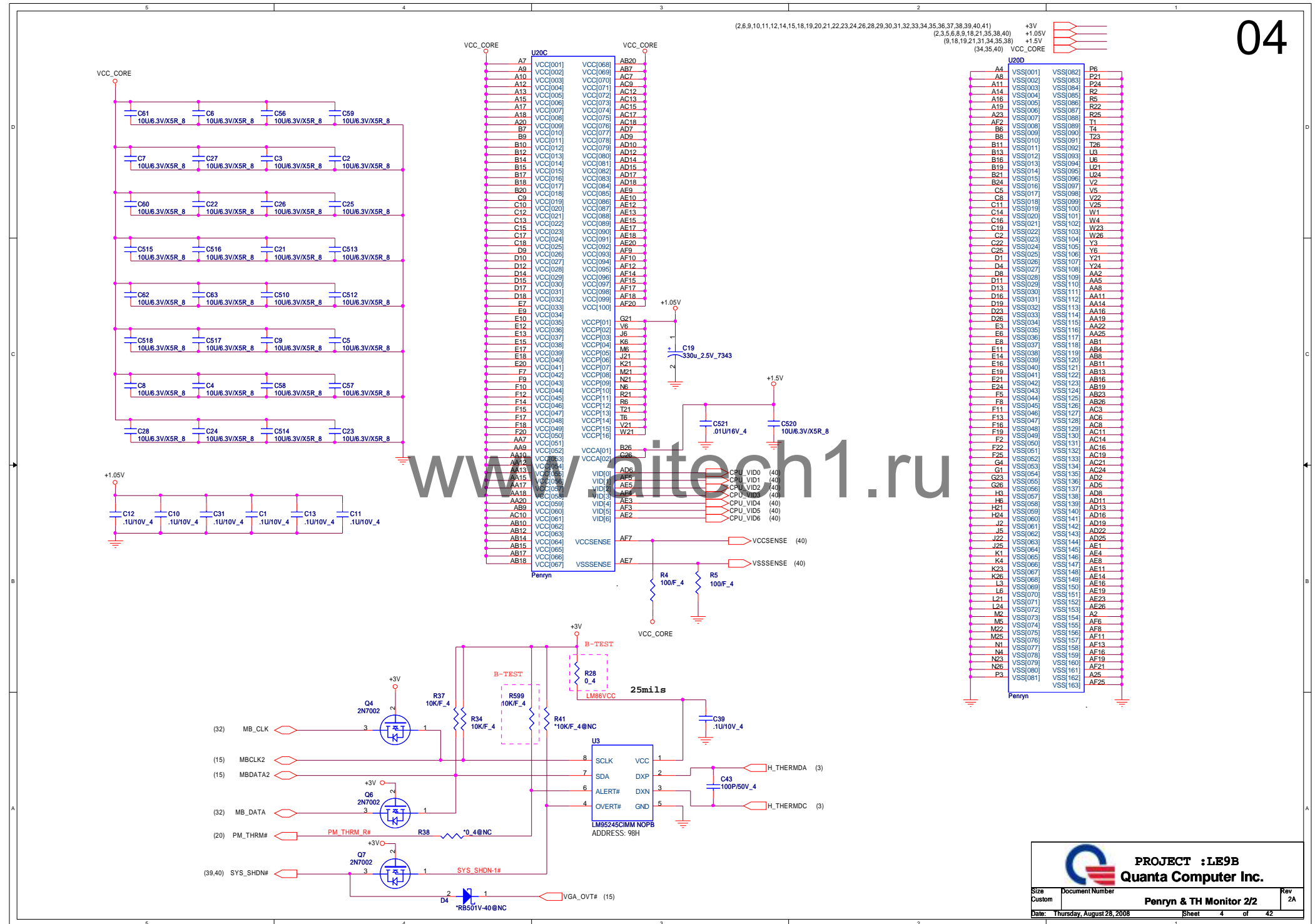
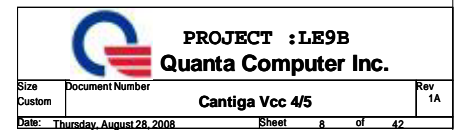


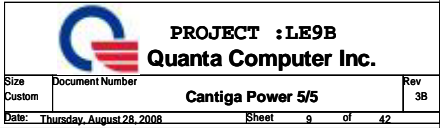
Populate ITP700Flex for bringup

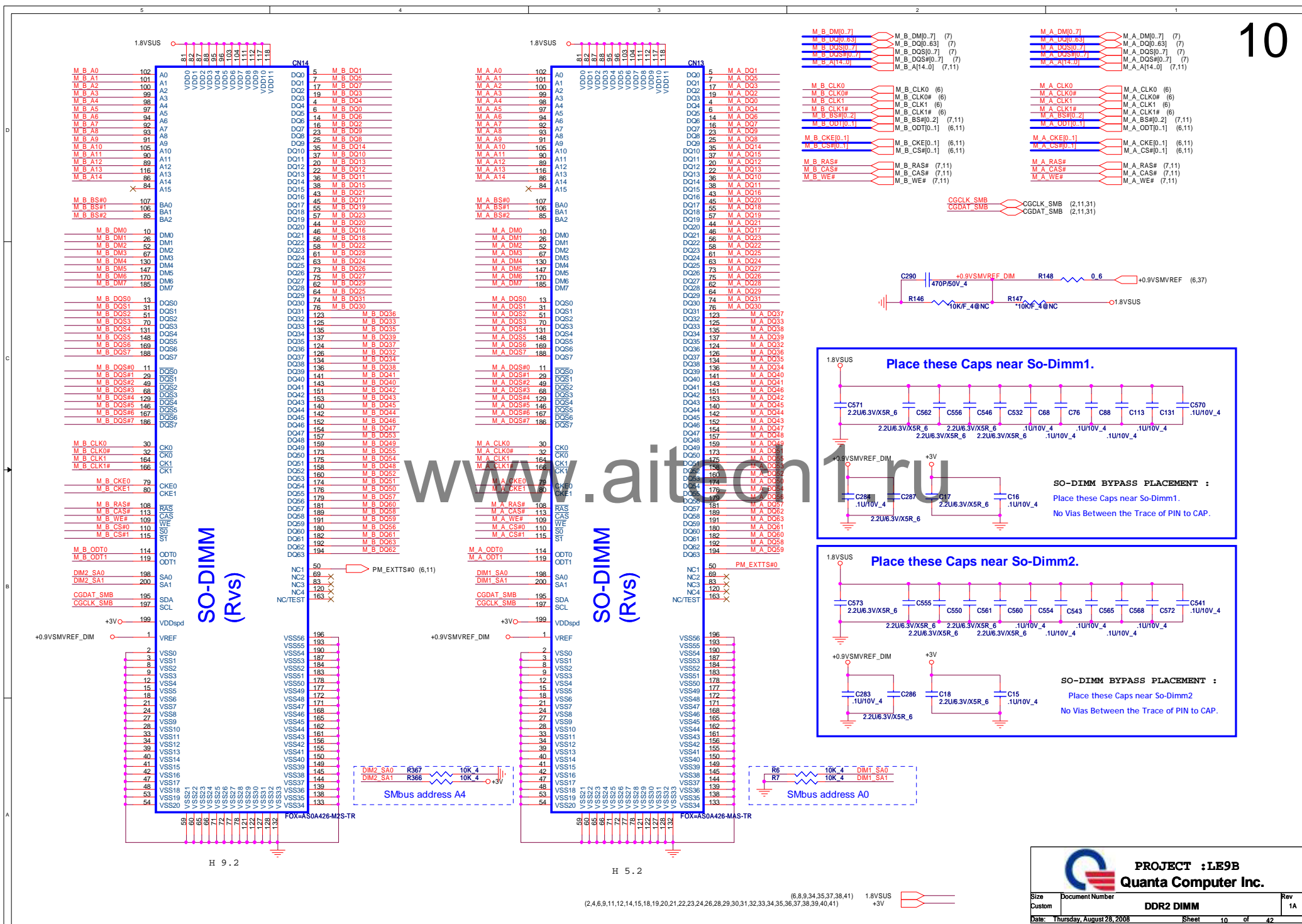






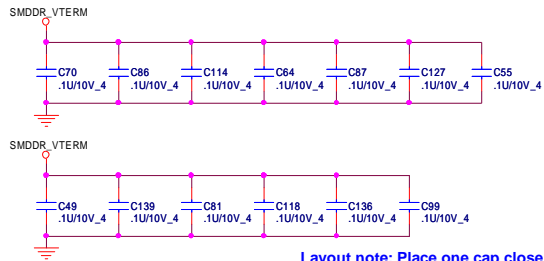




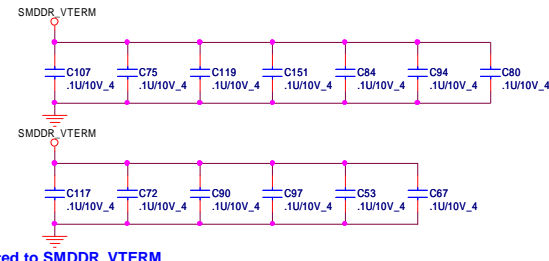


DDRII DUAL CHANNEL A,B.

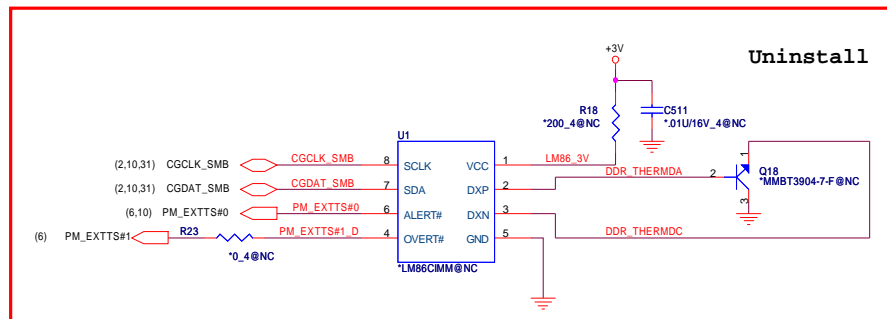
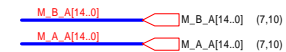
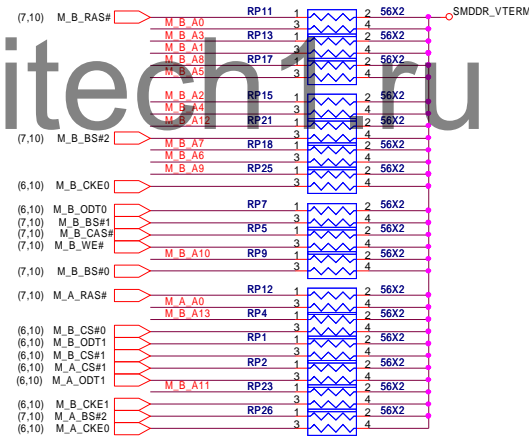
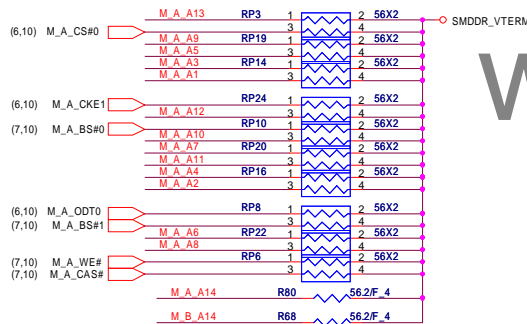
DDRII A CHANNEL



DDRII B CHANNEL

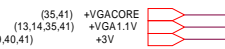


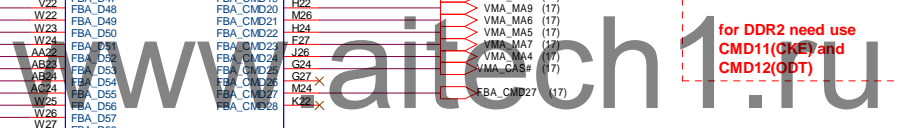
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM

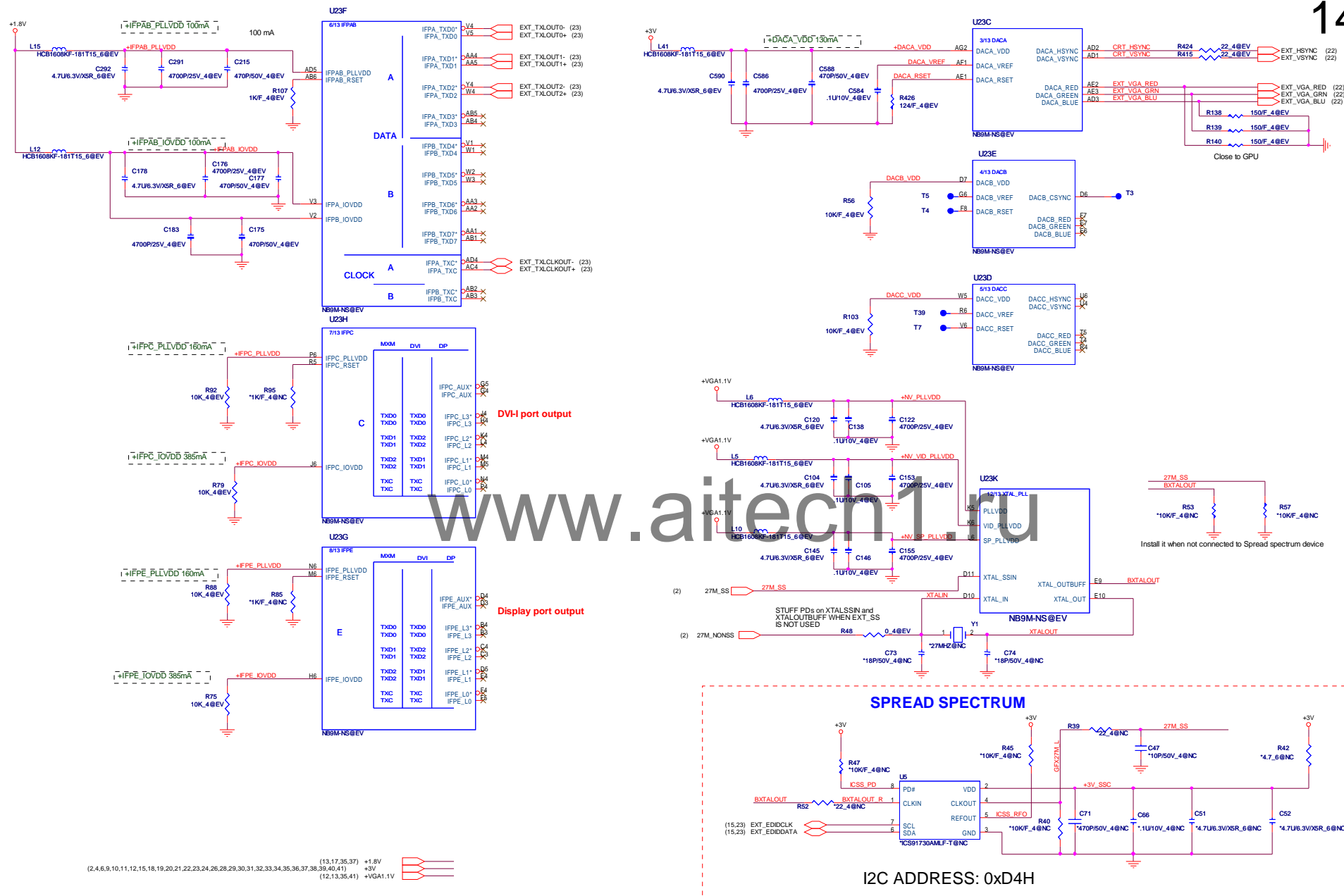


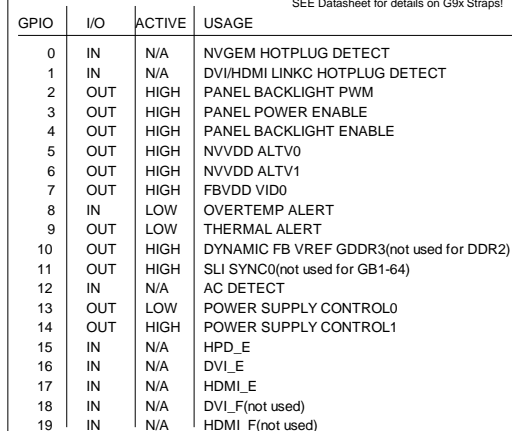
PROJECT : LE9B
Quanta Computer Inc.

Size	Document Number	Rev
Custom	DDR2 termination	1A
Date:	Thursday, August 28, 2008	Sheet 11 of 42









NB9M-NS VRAM Configuration Table

ROM_S1 VRAM CONFIG SET			
STRAP	VALUE		MEMORY Vendor
0	5K	PD	NOT USED
1	10K	PD	Samsung DDR2 16Mx16
2	15K	PD	Qmnd DDR2 16Mx16
3	20K	PD	HYNX DDR2 16Mx16
4	25K	PD	NOT USED
5	30K	PD	Samsung DDR2 32Mx16
6	34.8K	PD	Qmnd DDR2 32Mx16
7	45.3K	PD	HYNX DDR2 32Mx16

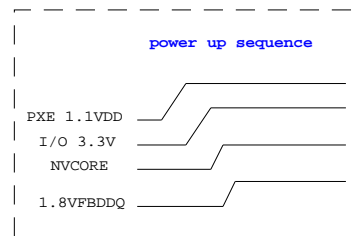
U23J


13/13 GND_NC

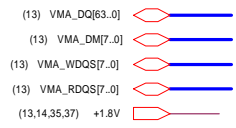
AC11	GND_01	NC_01	AA6
AC14	GND_02	NC_02	AC19
AC17	GND_03	NC_03	E15
AC2	GND_04	NC_04	T6
AC20	GND_05		
AC23	GND_06		
AC26	GND_07		
AC5	GND_08		
AC8	GND_09		
AF11	GND_10		
AF14	GND_11		
AF17	GND_12		
AF2	GND_13		
AF20	GND_14		
AF23	GND_15		
AF26	GND_16		
AF3	GND_17		
AF8	GND_18		
B11	GND_19		
B14	GND_20		
B17	GND_21		
B2	GND_22		
B20	GND_23		
B23	GND_24		
B26	GND_25		
B5	GND_26		
B8	GND_27		
E11	GND_28		
E14	GND_29		
E17	GND_30		
E2	GND_31		
E20	GND_32		
E23	GND_33		
E26	GND_34		
E5	GND_35		
E8	GND_36		
H2	GND_37		
H5	GND_38		
J11	GND_39		
J14	GND_40		
J17	GND_41		
K19	GND_42		
K9	GND_43		
L11	GND_44		
L12	GND_45		
L13	GND_46		
L14	GND_47		
L15	GND_48		
L16	GND_49		
L17	GND_50		
L2	GND_51		
L5	GND_52		
M2	GND_53		
M13	GND_54		
M14	GND_55		
M15	GND_56		
M16	GND_57		
P19	GND_58		
P2	GND_59		
P23	GND_60		
P26	GND_61		
P5	GND_62		
P9	GND_63		
T12	GND_64		
T13	GND_65		
T14	GND_66		
T15	GND_67		
T16	GND_68		
U11	GND_69		
U12	GND_70		
U13	GND_71		
U14	GND_72		
U15	GND_73		
U16	GND_74		
U17	GND_75		
U2	GND_76		
U23	GND_77		
U26	GND_78		
U5	GND_79		
V19	GND_80		
V9	GND_81		
W11	GND_82		
W14	GND_83		
W17	GND_84		
Y2	GND_85		
Y23	GND_86		
Y26	GND_87		
Y5	GND_88		

NB9M-NS@EV

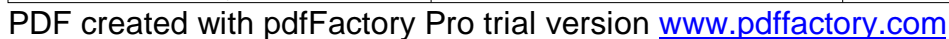
NB9M: VGACORE +0.9V ~ +1.0V

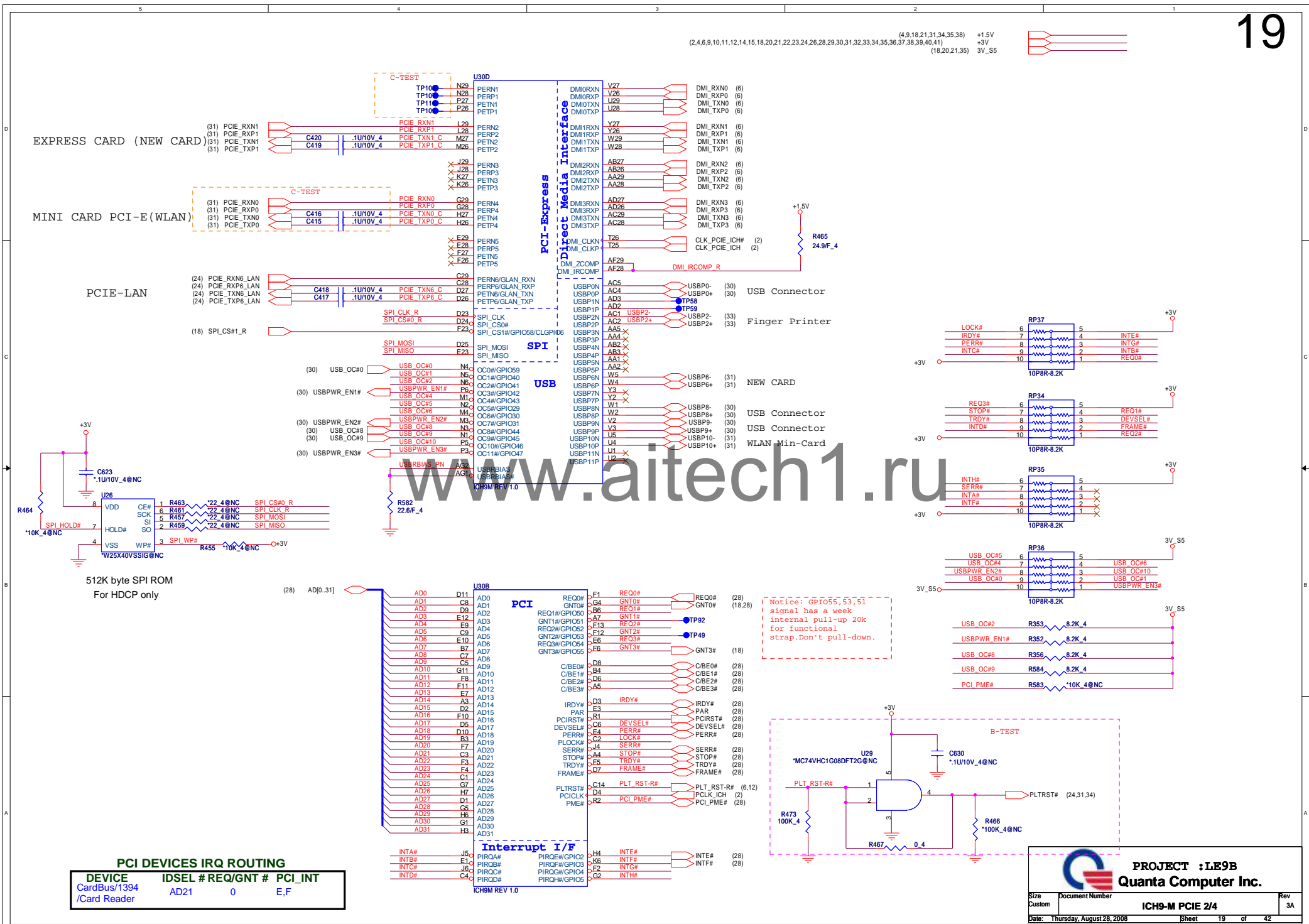


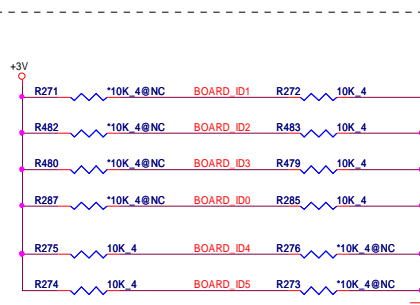
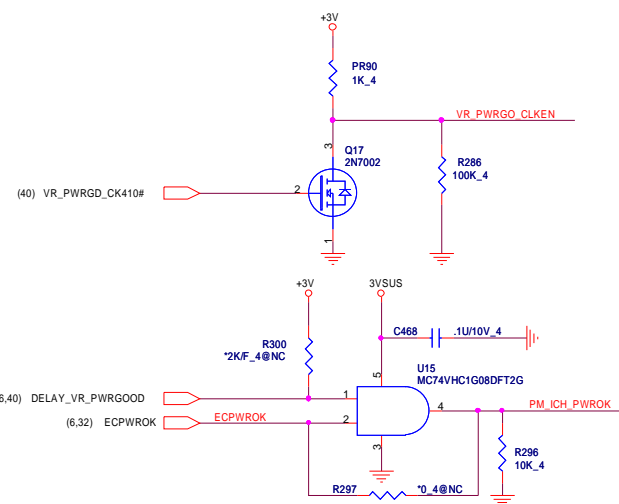
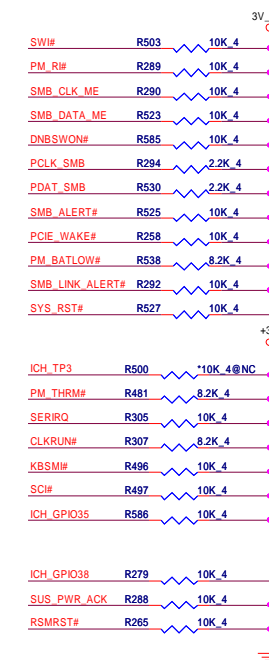
 PROJECT :LE9B Quanta Computer Inc.		Rev
		1A
Size	Document Number	
Custom	NV9X GND 5/5	
Date	Thursday, August 28, 2008	Sheet 16 of 42



5162FFR-25C@EV
C-TEST







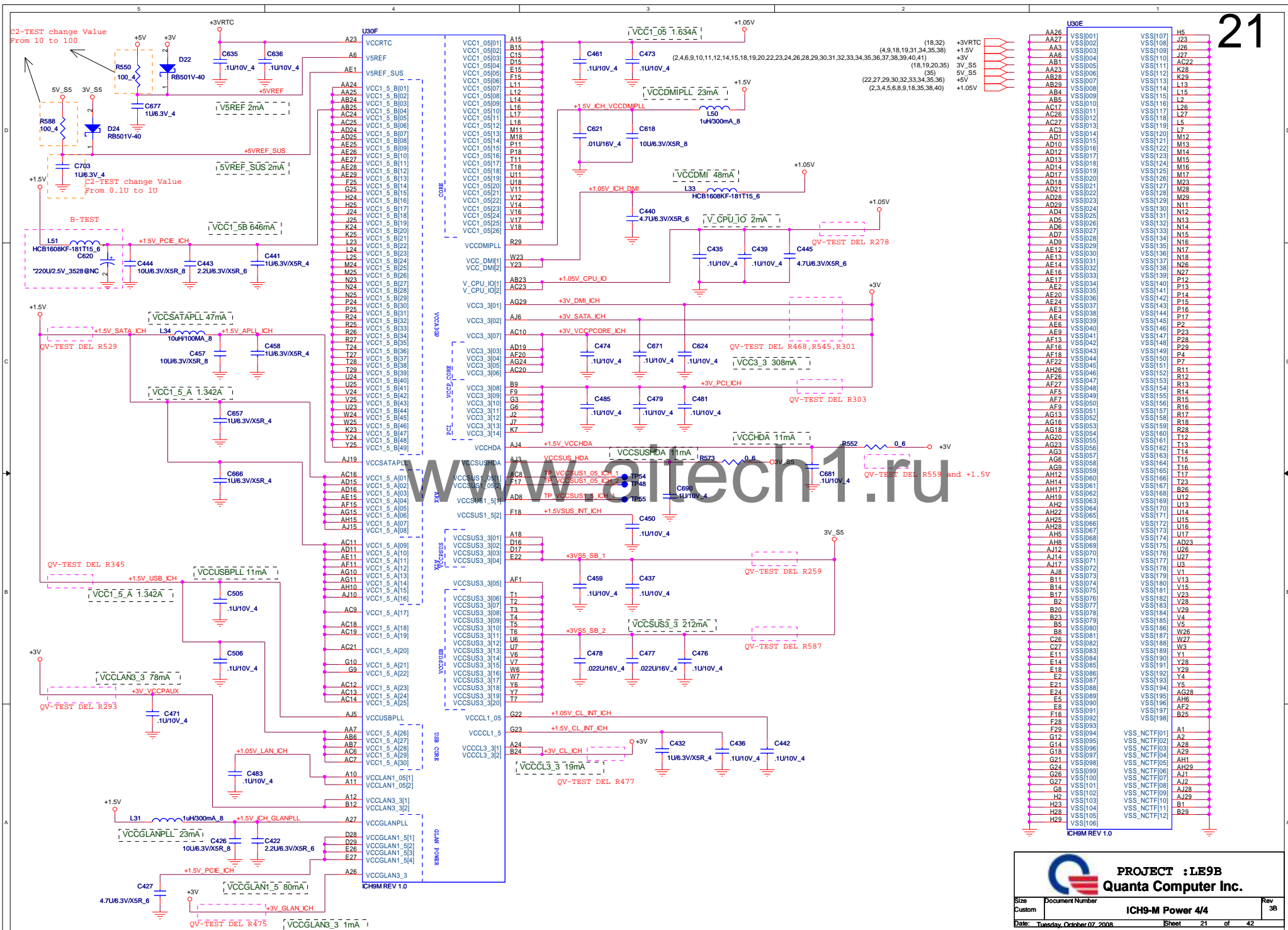
Board ID For Function	ID3 GPIO22	ID2 GPIO21	ID1 GPIO19	ID0 GPIO1
Default	0	0	0	0
GL40	0	1	0	0
Without MDC	1	0	0	0

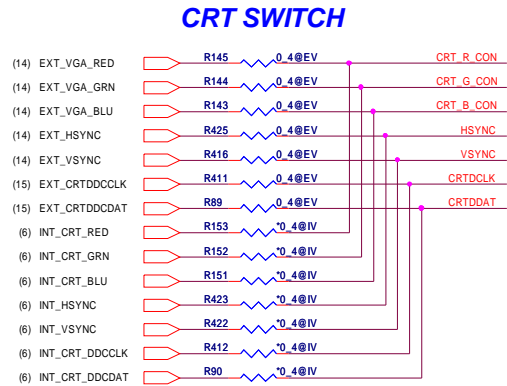
Board ID For Model	ID5 GPIO37	ID4 GPIO3
LE6	0	0
LE7	0	1
LE8	1	0
LE9	1	1



PROJECT :LE9B
Quanta Computer Inc.

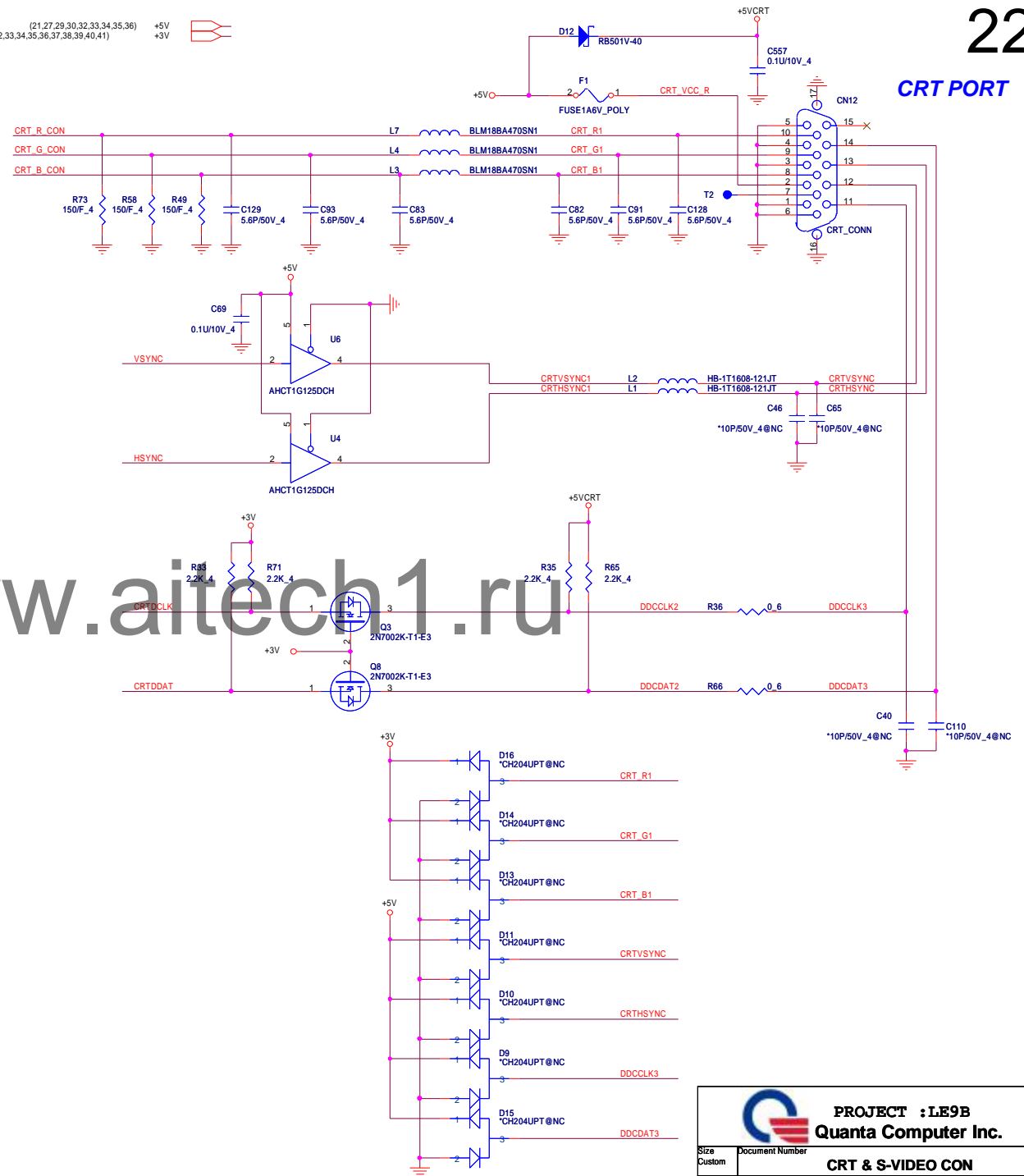
Size	Document Number	Rev
Custom	ICH9-M GPIO 3/4	3B
Date: Tuesday, October 14, 2008	Sheet 20 of 42	

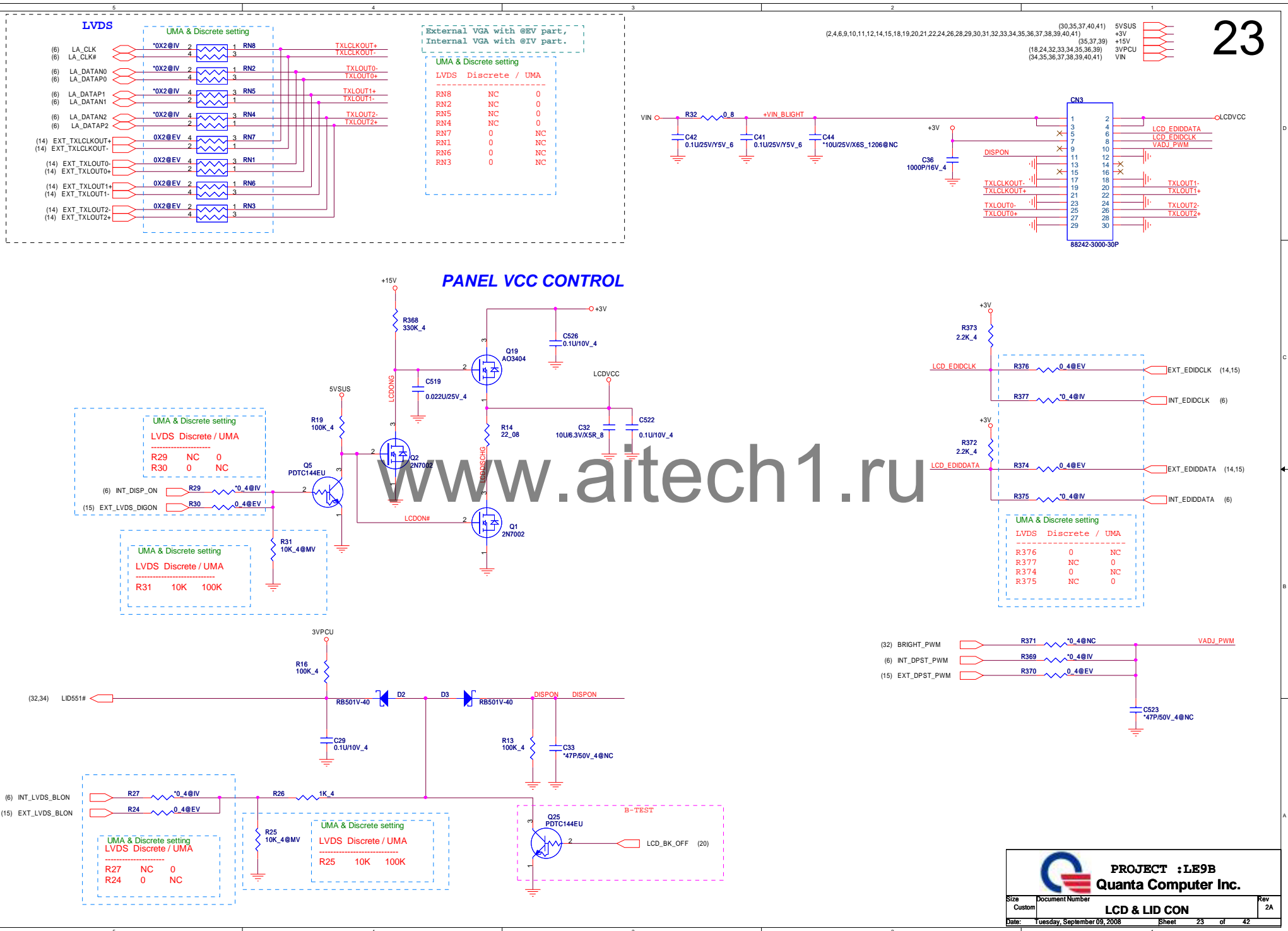


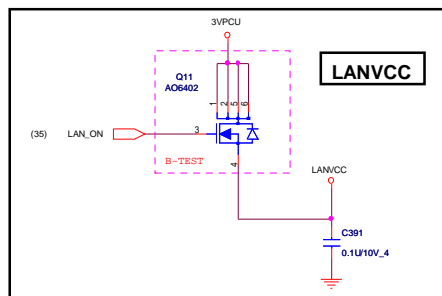


UMA & Discrete setting
LVDS Discrete / UMA

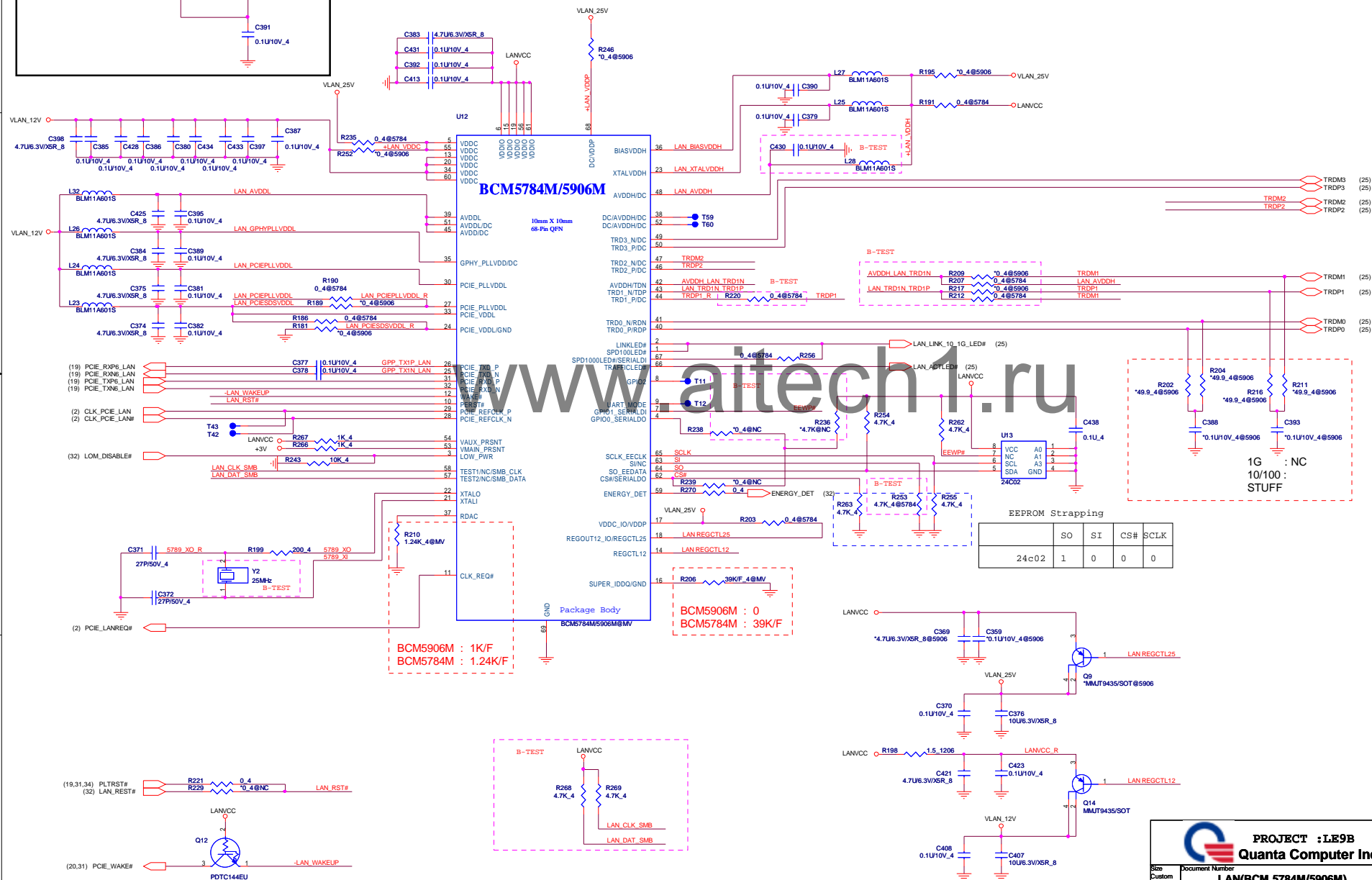
R145	0	NC
R144	0	NC
R143	0	NC
R425	0	NC
R416	0	NC
R411	0	NC
R89	0	NC
R153	NC	0
R152	NC	0
R151	NC	0
R423	NC	0
R422	NC	0
R412	NC	0
R90	NC	0

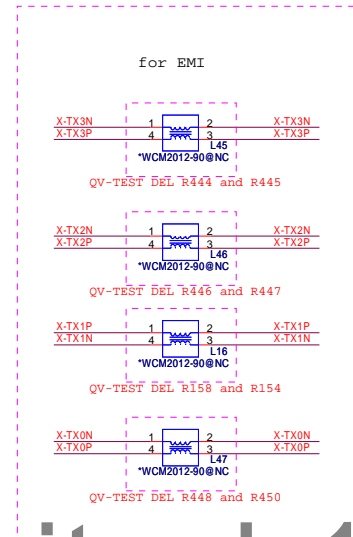
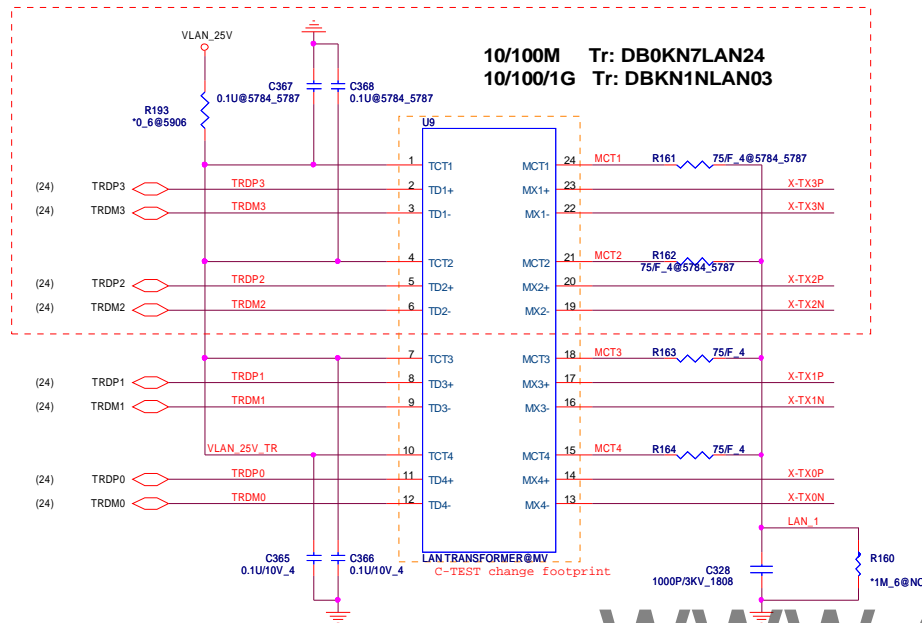




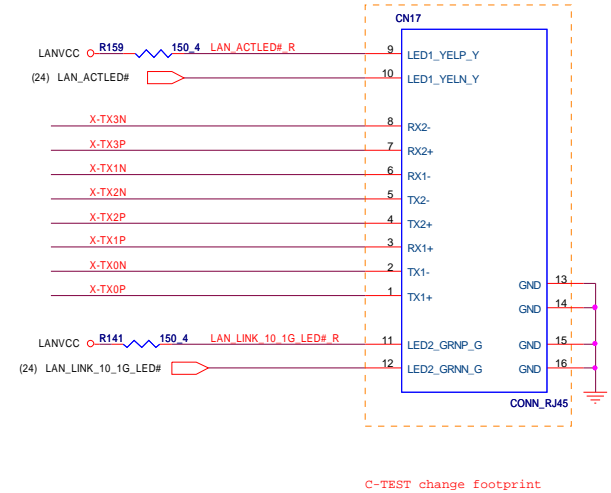


BCM5784M(LAN 10/ 100/ 1G) ==>
BCM5906M(LAN 10/ 100) ==>
AJ059060000

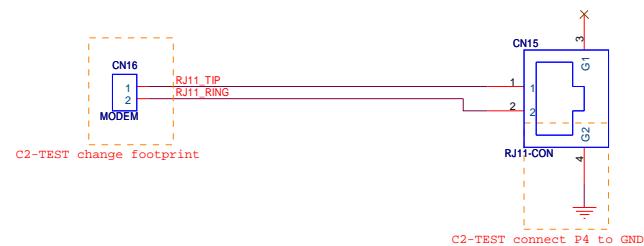




RJ45 Connector

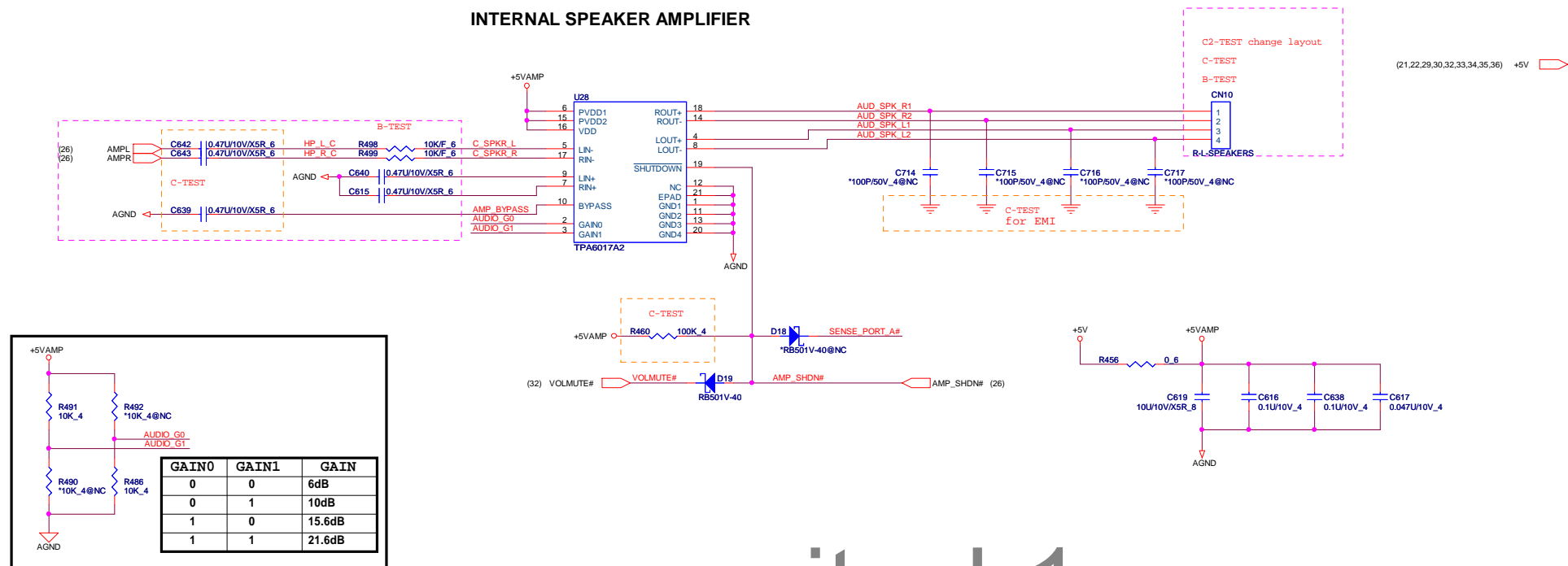


RJ11 Connector

PROJECT : LE9B
Quanta Computer Inc.

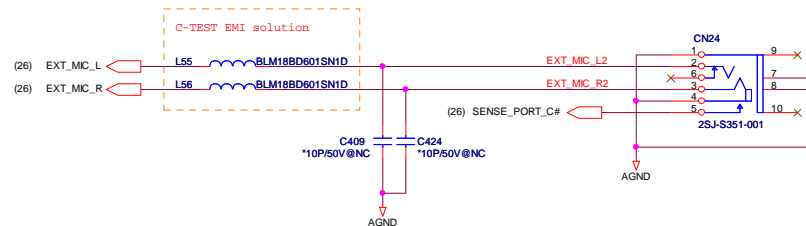
Size	Document Number	Rev
Custom	RJ11/RJ45 CONNECTOR	38
Date:	Tuesday, September 23, 2008	Sheet 25 of 42

INTERNAL SPEAKER AMPLIFIER

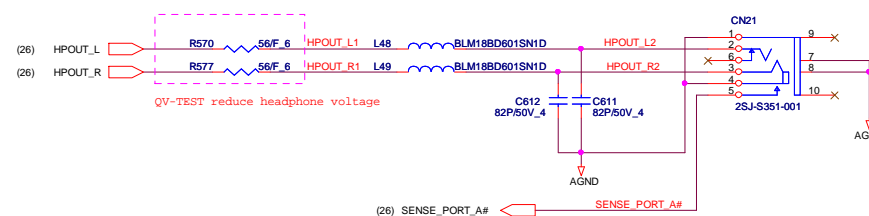


www.aitech1.ru

MIC-IN JACK



HEADPHONE



PROJECT :LE9B
Quanta Computer Inc.
AMP_MAX9789A & JACK

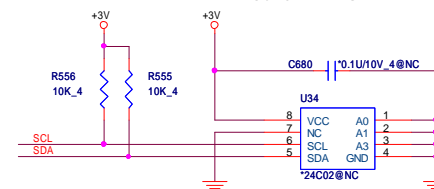
Size Custom Document Number Rev 3B
Date: Thursday, August 28, 2008 Sheet 27 of 42

(2,4,6,8,10,11,12,14,15,18,19,20,21,22,23,24,26,29,30,31,32,33,34,35,36,37,38,39,40,41)

+3V

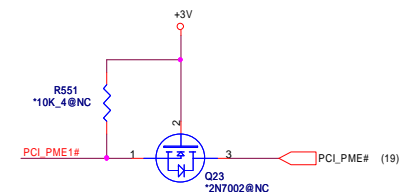
PCI/OTHER

Serial EEPROM



* NOT Use EEPROM :
R7116 : installed (57 pin pull hi)
R7115,U128,C7071 : NOT installed

* Use EEPROM :
R7115,U128,C7071 : installed
R7116 : NOT installed (57 pin pull low)



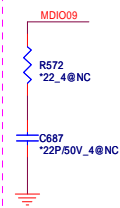
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CoreLogic CLOCKRUN#

When CLKRUN# is controlled by system, the pull-down resistor(R14) dose not need to apply.

IEEE1394/MEM CARD

for EMI



R572 22.4@NC

C687 22P/50V_4@NC

R572 22.4@NC

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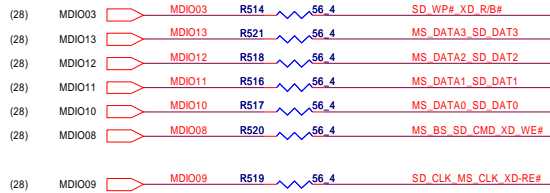
C687 22P/50V_4@NC

R572 22.4@NC

C687 22P/50V_4@NC

R572 22.4@NC

C687

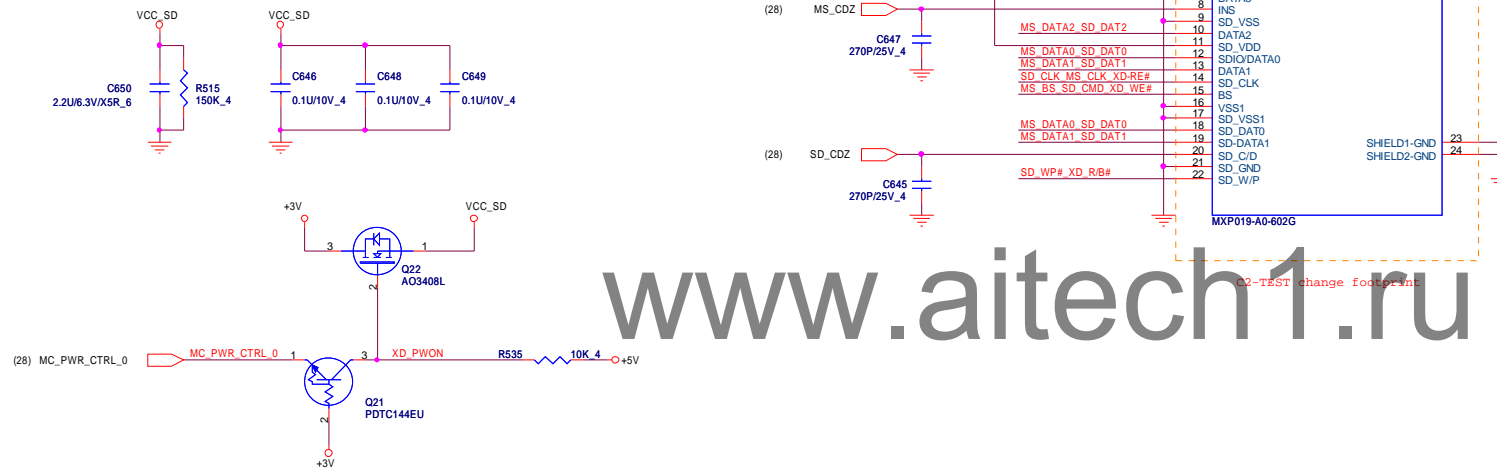


(2,4,6,9,10,11,12,14,15,16,19,20,21,22,23,24,26,28,30,31,32,33,34,35,36,37,38,39,40,41)
(21,22,27,30,32,33,34,35,36)

+3V
+5V

3 IN 1 CARD-READER (PUSH-PUSH)

Support MMC/SD/MS Cards



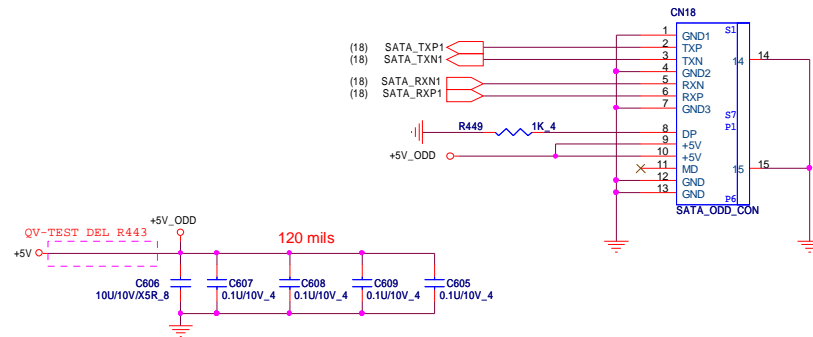
www.aitech1.ru



PROJECT : LE9B
Quanta Computer Inc.

Size	Document Number	Rev
	3 IN 1 CR SOCKET	3B
Date:	Thursday, August 28, 2008	Sheet 29 of 42

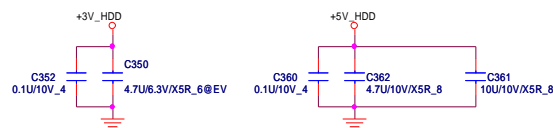
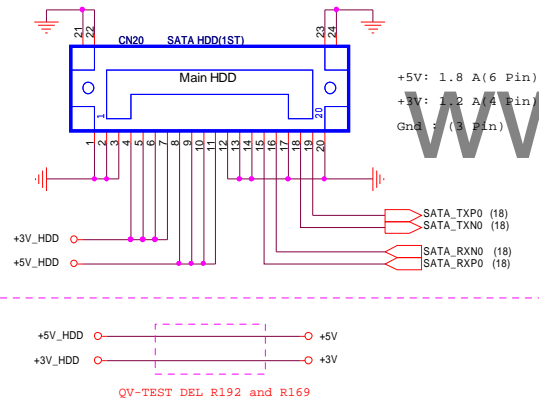
SATA CD-ROM



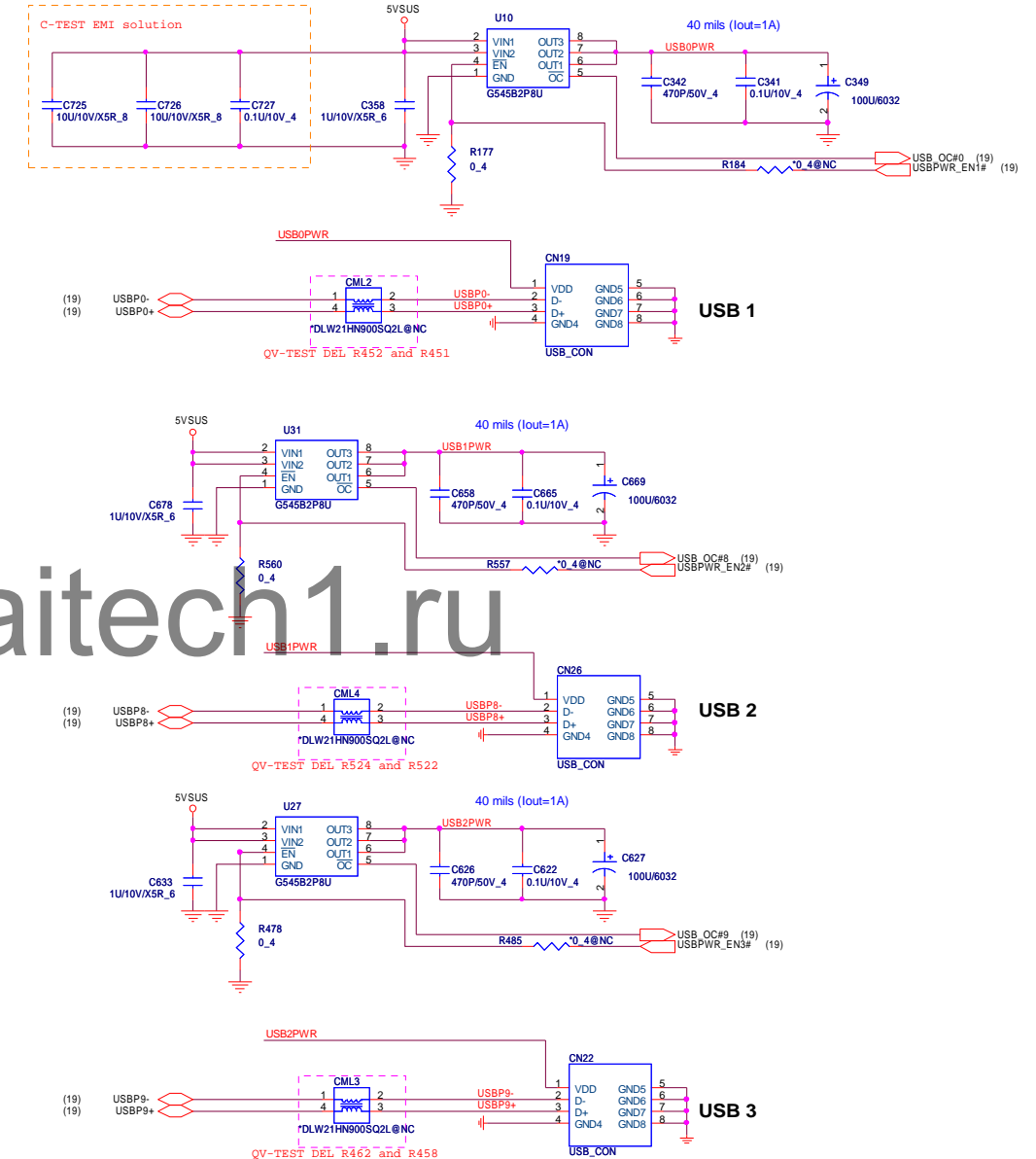
SATA-HDD CONNECTOR

B-TEST
Modify mil > Size as SMT request

DC Current rating: 0.5 A



USBX3

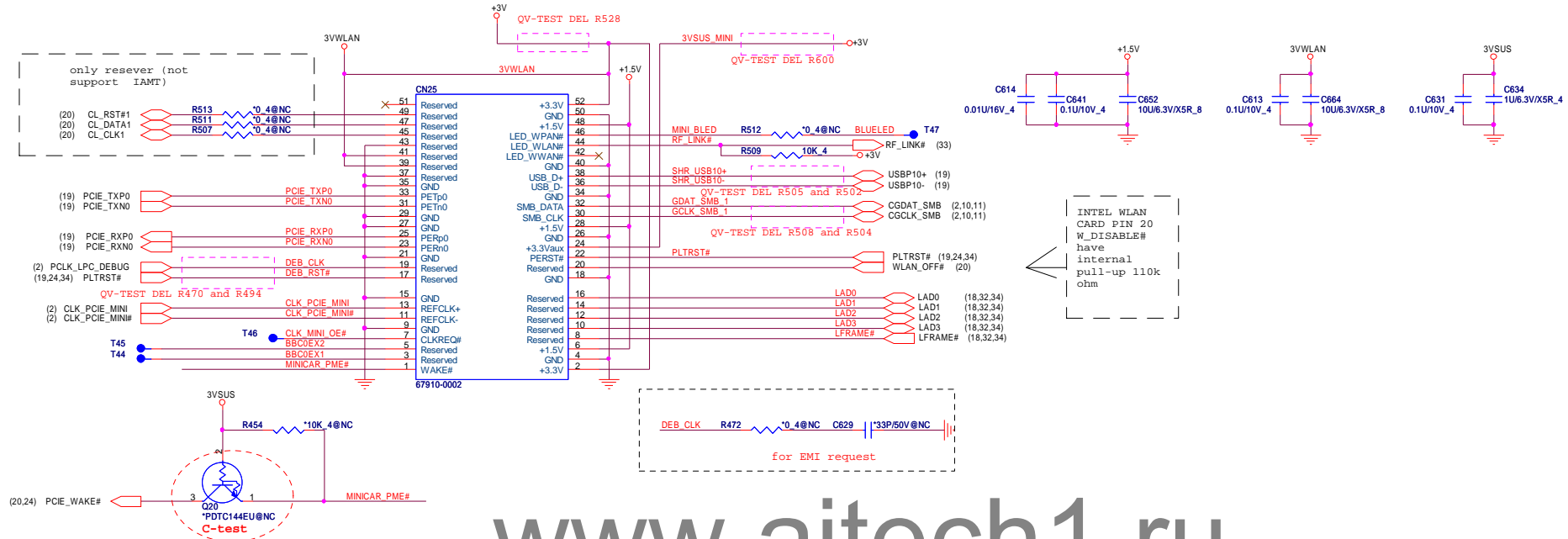


PROJECT : LE9B
Quanta Computer Inc.

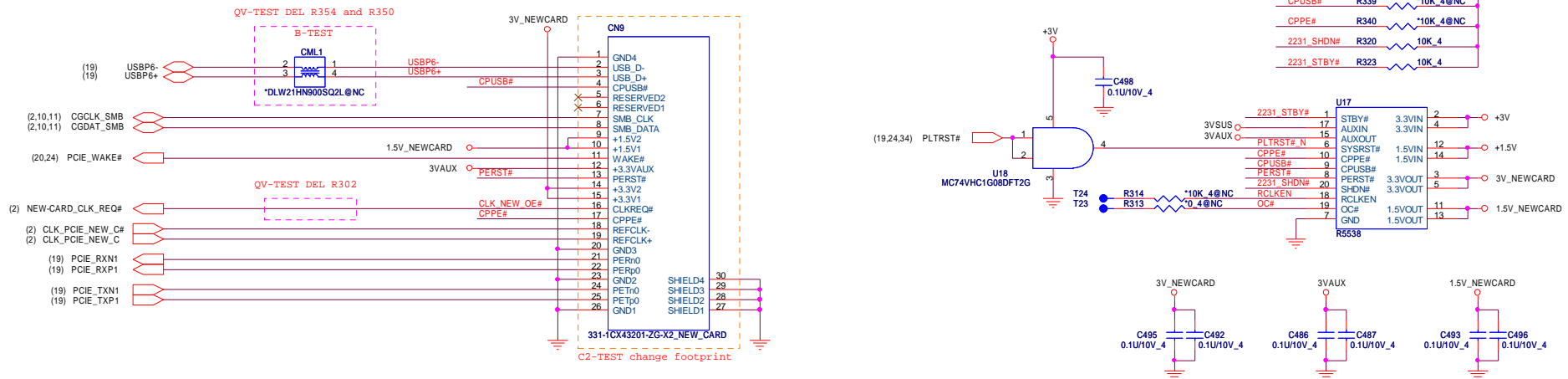
Size	Document Number	Rev
Custom	SATA HDD/CD-ROM/USBX3	38
Date:	Wednesday, September 10, 2008	Sheet 30 of 42

Mini PCI-E Card 1 WLAN

31

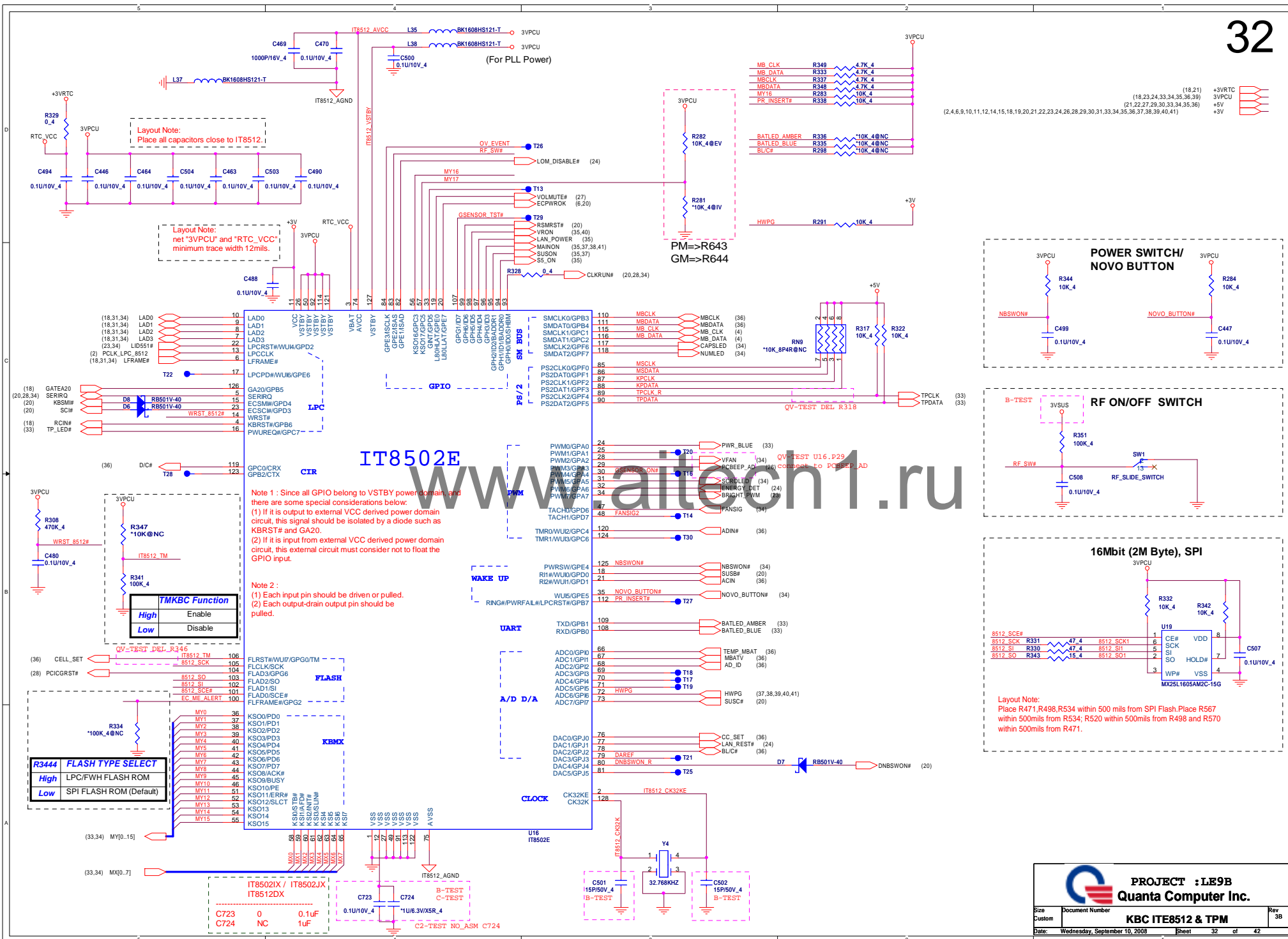


Express Card

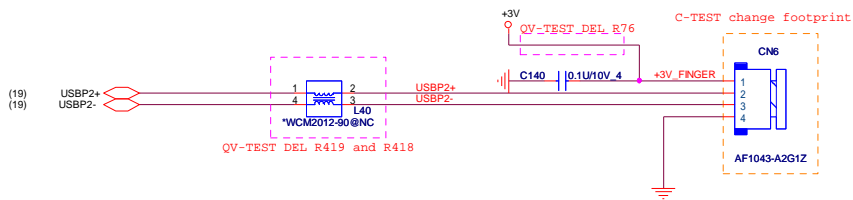


PROJECT : LE9B
Quanta Computer Inc.

Size	Document Number	Rev
Custom	WLAN & Express Card	3B
Date:	Thursday, August 28, 2008	Sheet 31 of 42

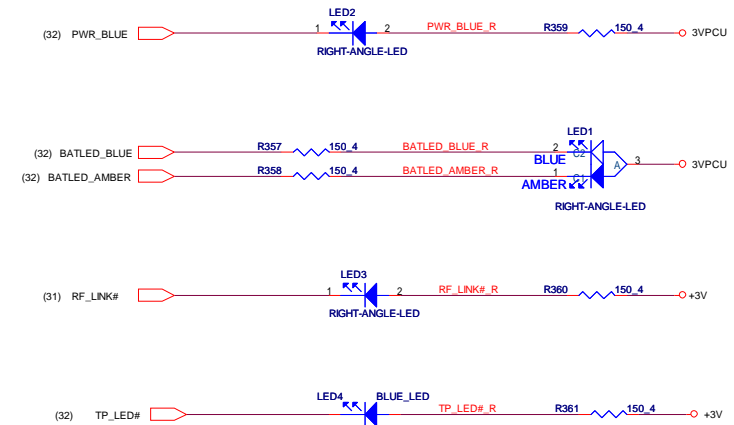


FINGER PRINTER

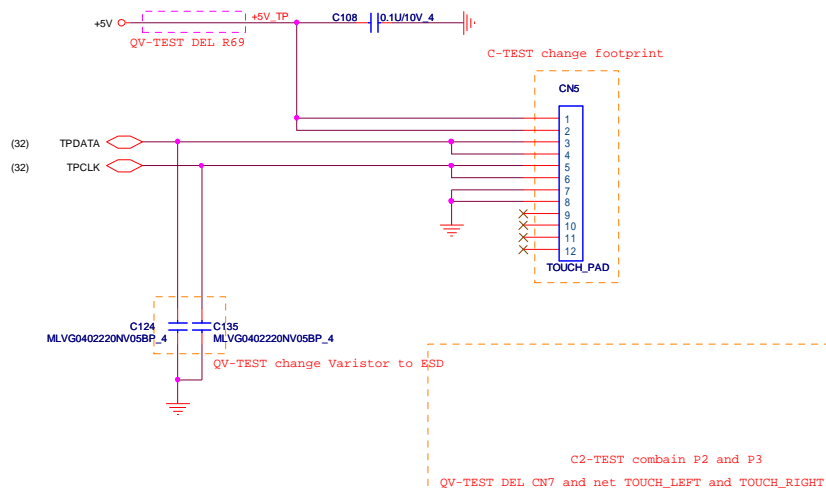


(2,4,6,9,10,11,12,14,15,18,19,20,21,22,23,24,26,28,29,30,31,32,34,35,36,37,38,39,40,41)
 +3V
 +3V
 3VPCU
 (18,23,24,32,34,35,36,39)

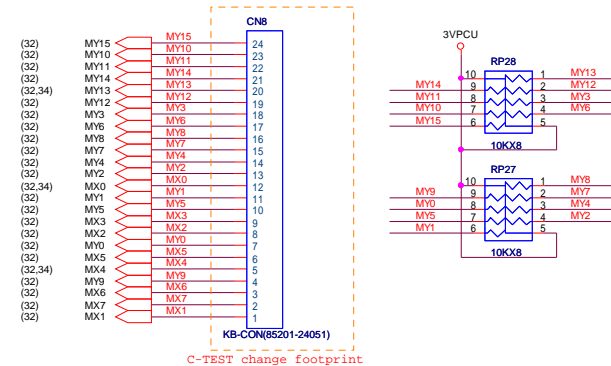
LED



TOUCH PAD



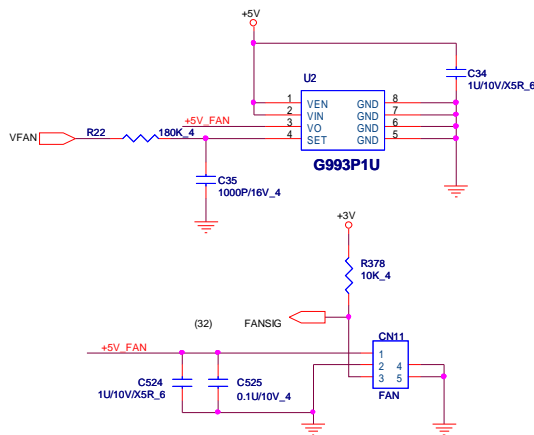
KEYBOARD



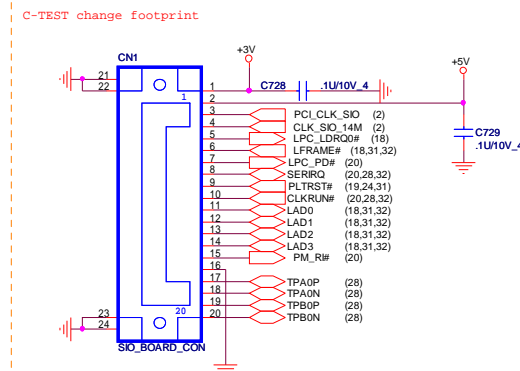
PROJECT :LE9B
 Quanta Computer Inc.

Size Custom Document Number F/P,LED,K/B,T/P Rev 3B
 Date: Thursday, August 28, 2008 Sheet 33 of 42

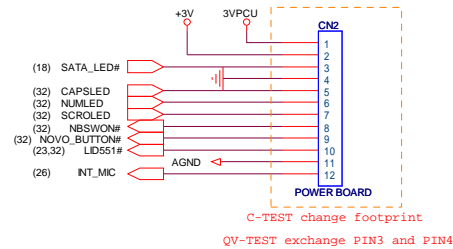
FAN CONTROL



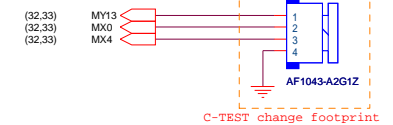
SIO BOARD



POWER BOARD



Function BUTTON BOARD



For Modem Cable Holders

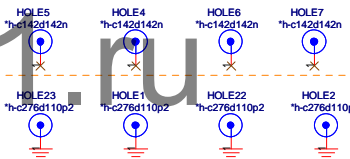
C-TEST delete modem cable holder



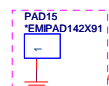
C-TEST delete modem cable holder



C2-TEST change to not PTH



C-TEST delete WLAN & LCD cable clip

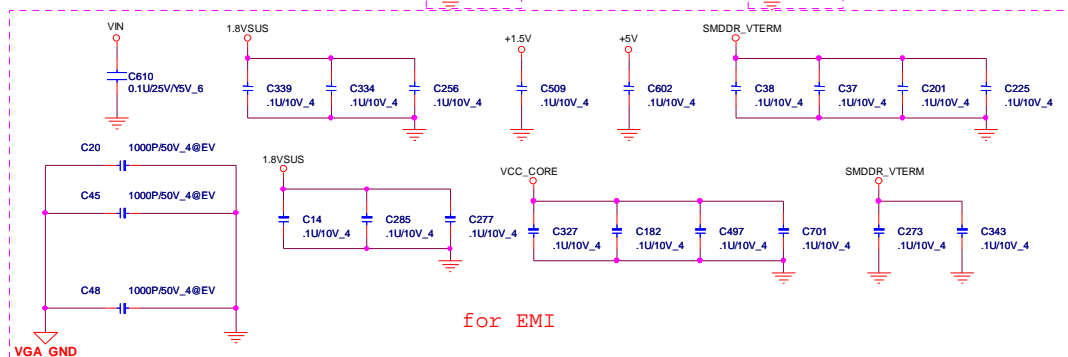


QV-TEST Add PAD15 for MDC



QV-TEST Add PAD14 for EMI

QV-TEST change to AGND



for EMI

for EMI IN QV STAGE

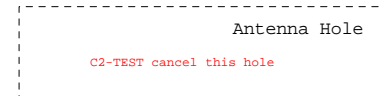
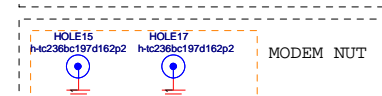
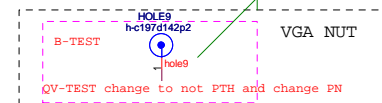
B-TEST



QV-TEST DEL HOLE19 and HOLE20



C2-TEST add



add hole strain

QV-TEST change to not PTH and change PN

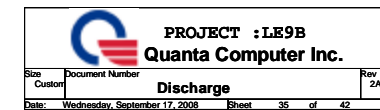
C2-TEST change footprint

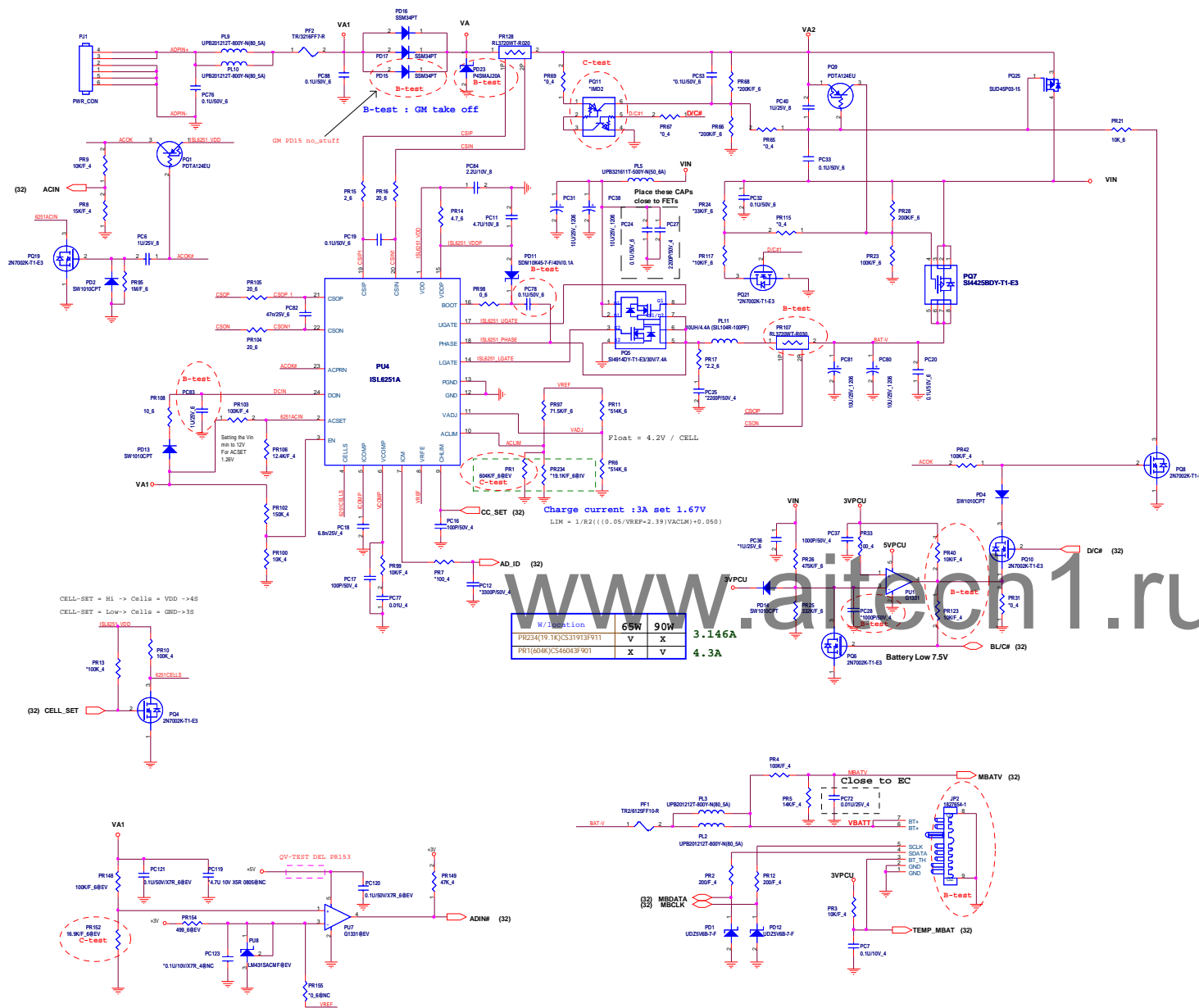
C2-TEST change footprint

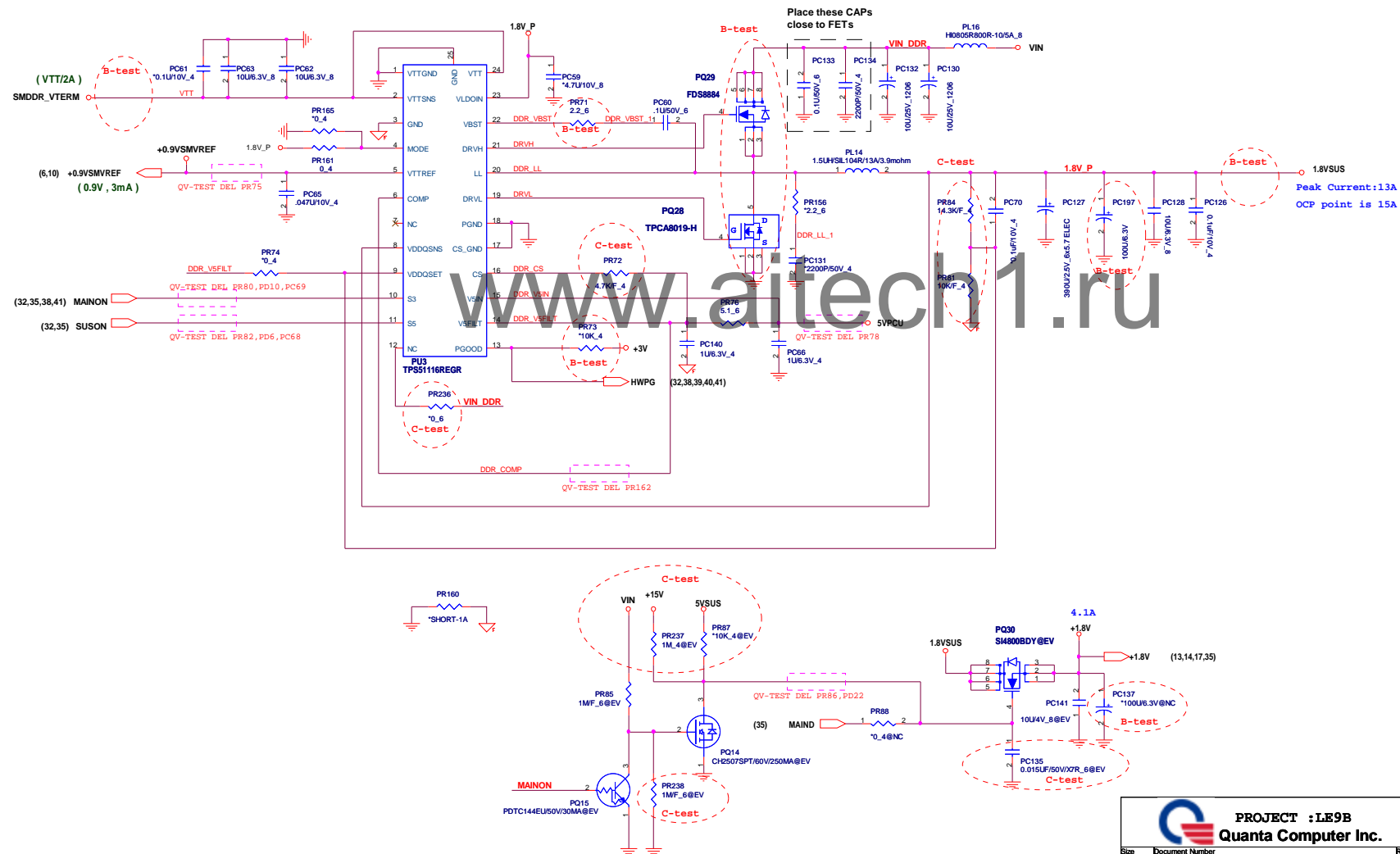
C2-TEST cancel this hole

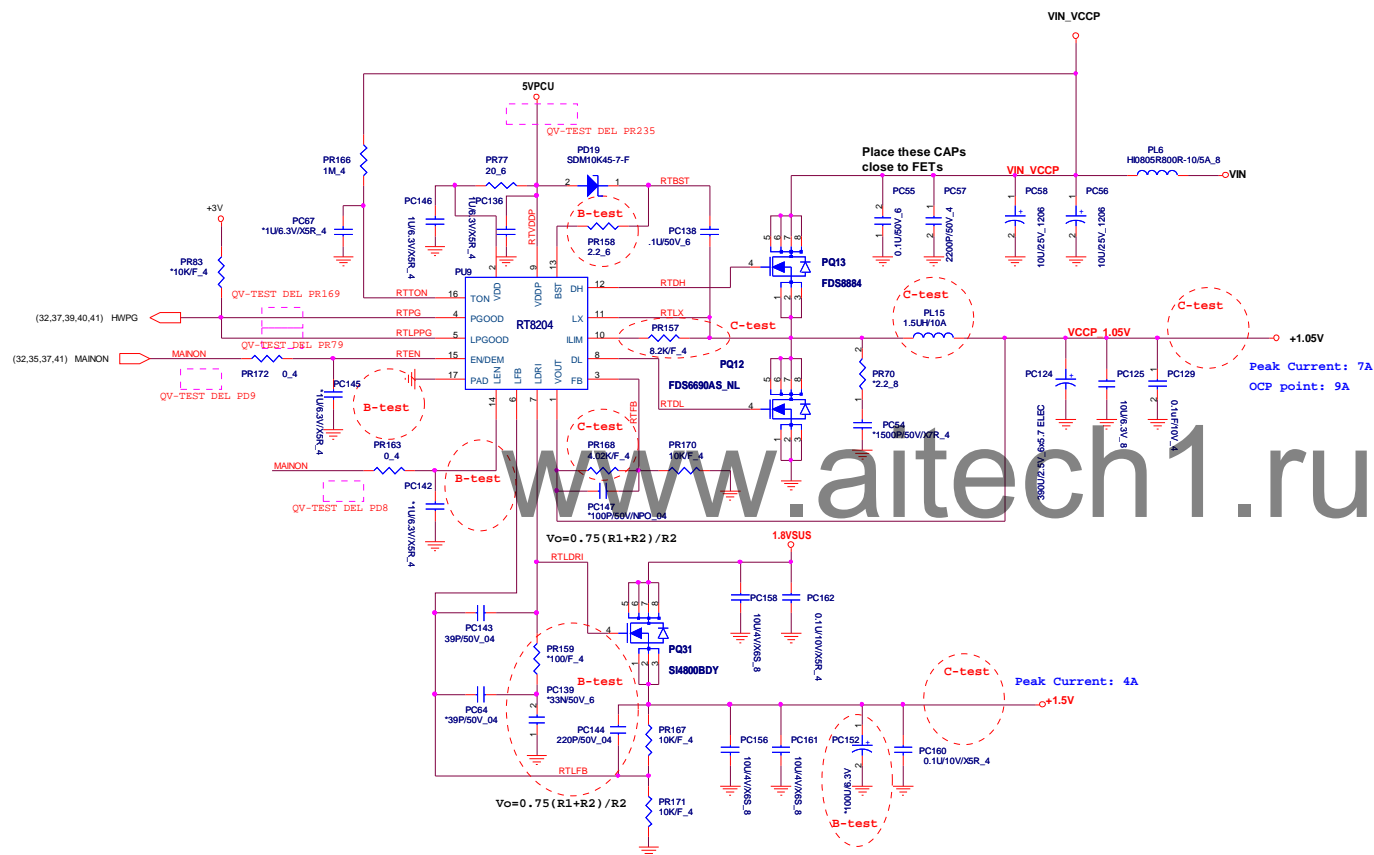
PROJECT :LE9B
Quanta Computer Inc.

Size	Document Number	Rev
Custom	B to B CON,Fan CON,HOLE	38
Date:	Thursday, August 28, 2008	Sheet 34 of 42



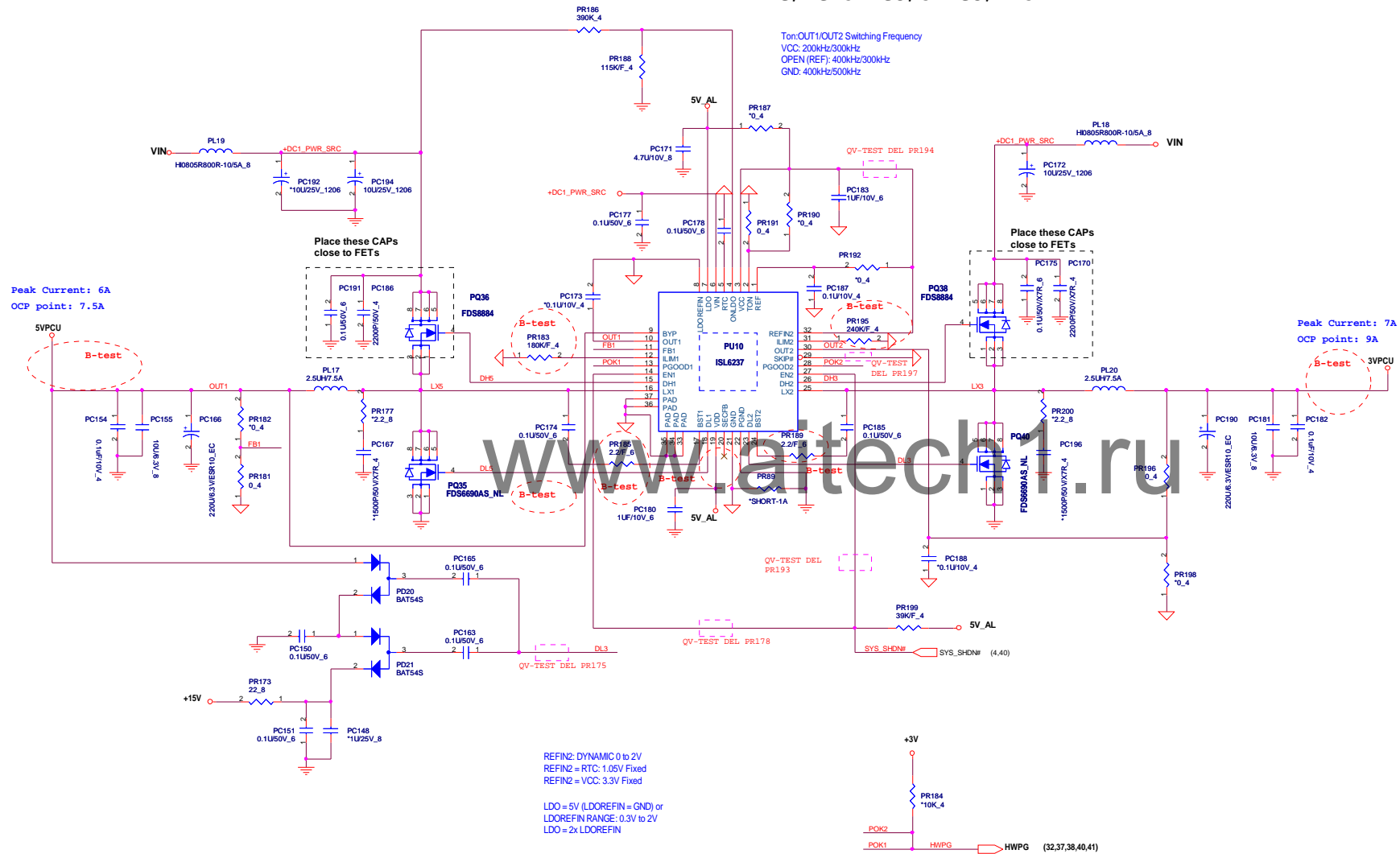


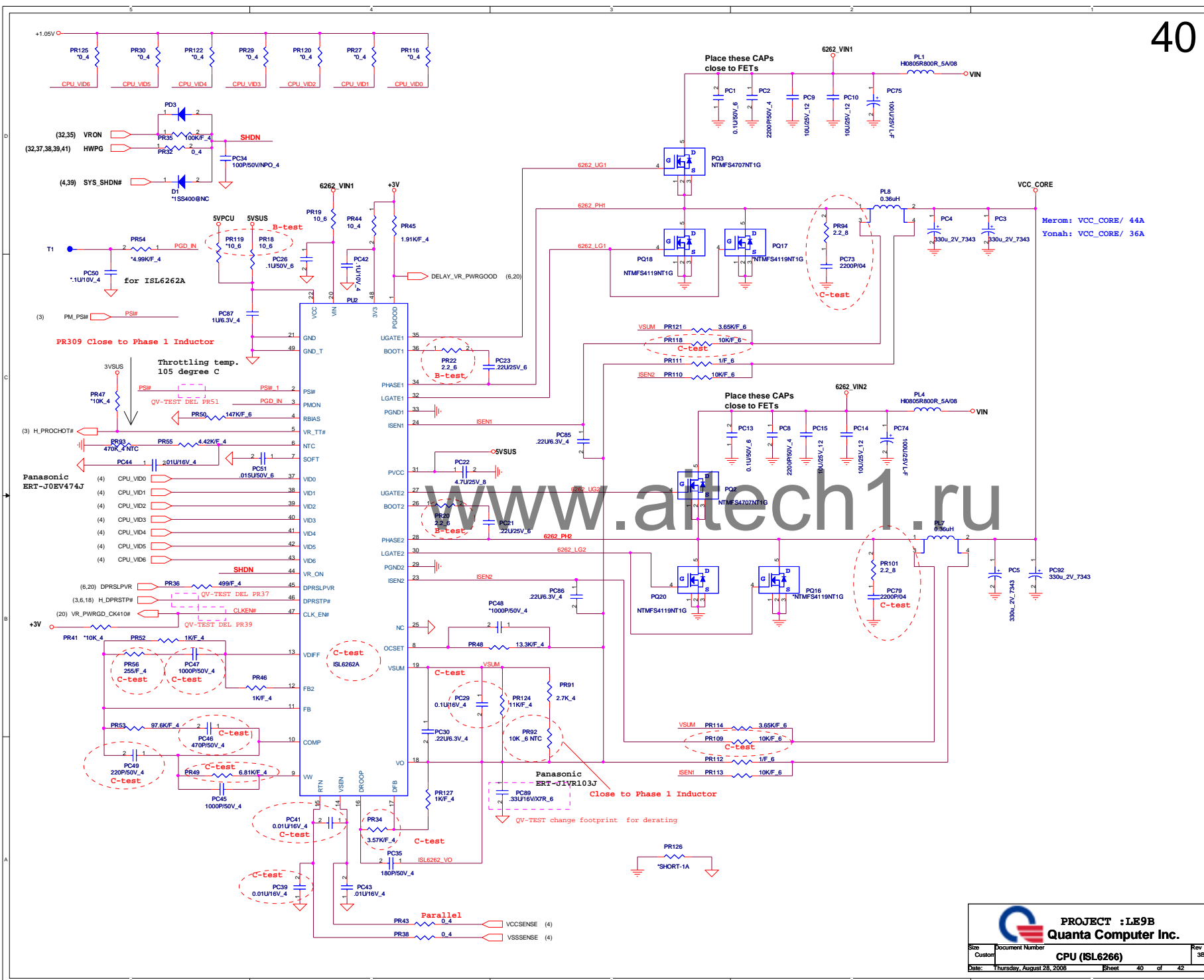


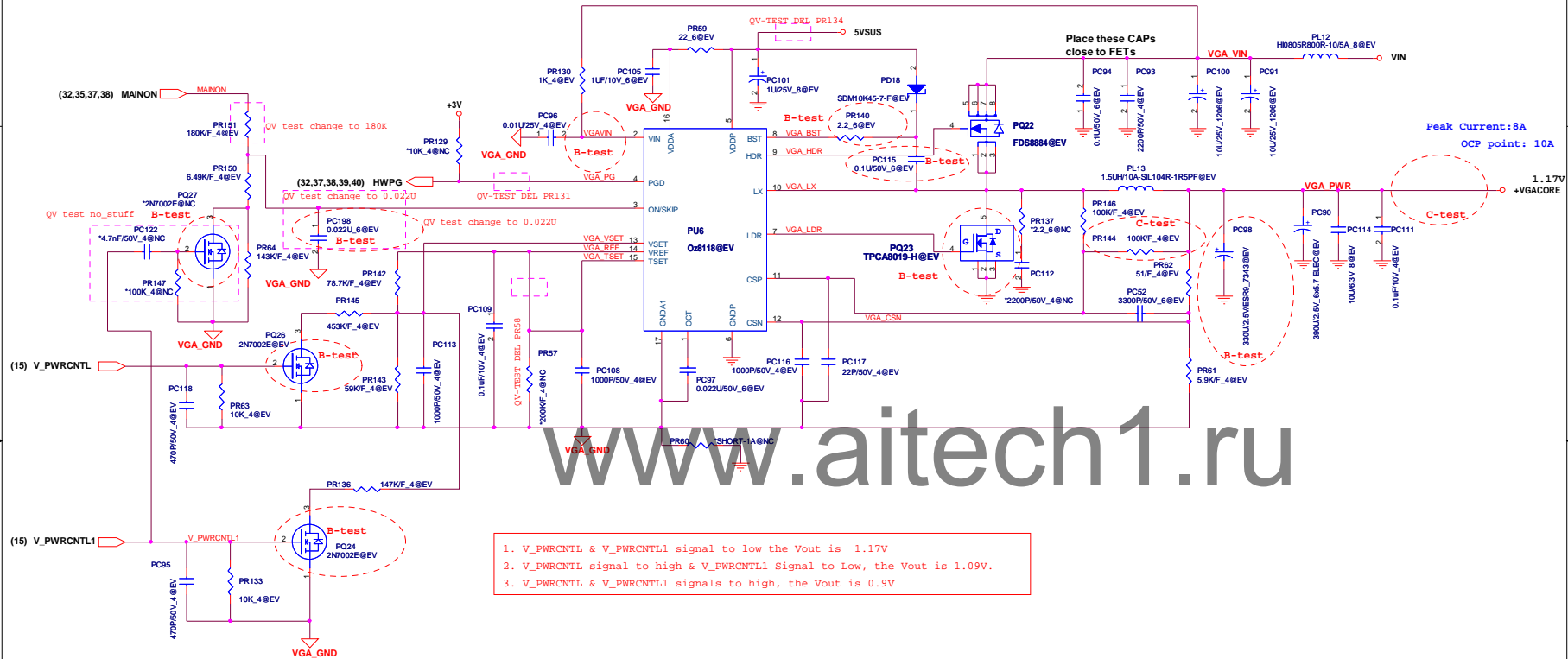


DC/DC 3VPCU/5VPCU/+15V

Ton:OUT1/OUT2 Switching Frequency
VCC: 200kHz/300kHz
OPEN (REF): 400kHz/300kHz
GND: 400kHz/500kHz

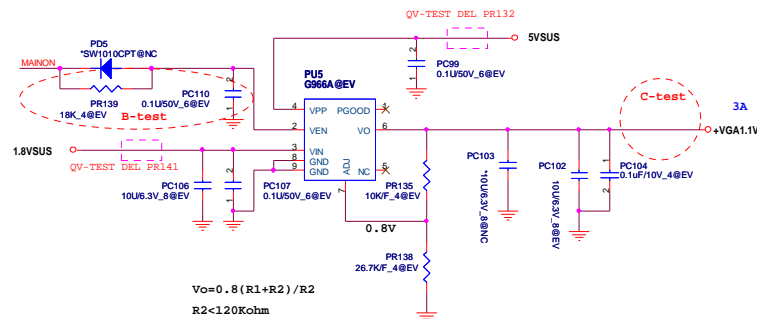






1. V_PWRCNTL & V_PWRCNTL1 signal to low the Vout is 1.17V
2. V_PWRCNTL signal to high & V_PWRCNTL1 Signal to Low, the Vout is 1.09V.
3. V_PWRCNTL & V_PWRCNTL1 signals to high, the Vout is 0.9V

V_PWRCNTL1	V_PWRCNTL	Vout (spec)
0	0	1.17V
0	1	1.09V.
1	0	0.95V
1	1	0.9V



LE9 SYSTEM POWER BLOCK DIAGRAM

